

DETAILED ACTION

Response to Arguments

1. Applicant's arguments and amendments, see Remarks, filed 6/22/2009, with respect to claims 2-7 and 15-18 have been fully considered and are persuasive. The rejection of claims 2-7 and 15-18 has been withdrawn.

Response to Amendment

2. Claims 2 and 13 have been amended.
3. Claims 1 and 11-14 have been cancelled..
4. Claims 3 and 8-10 have been cancelled.
5. Claims 2-7 and 15-18 are pending.

Claim Objections

6. In light of Applicant's amendment, the previous objection to claims 2 and 12 has been withdrawn.

Claim Rejections - 35 USC § 112

7. In light of Applicant's amendment, the previous 35 USC 112, 1st and 2nd paragraph rejections of claim 2 have been withdrawn.

EXAMINER'S AMENDMENT

8. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in email messages with Tanya Nair and Simon Walmsley on 9/17/2009 and 9/23/2009.

Please amend the claims as follows:

Claim 1. (Cancelled)

Claim 2. (Currently Amended) A method of attempting a write to a target entity having first and second security fields to cause the target entity to perform an action, the method including the steps of:

initializing the first security field with a first initial value and the second security field with a second initial value;

sending a first message to the target entity that is an integrated circuit, the first message being configured to cause the entity to perform the action and to adjust the first and second initial values in the first and second security fields by respective first and second amounts resulting in respective first and second adjusted values;

sending a second message to the target entity, the second message being configured to cause the entity to adjust the first and second initial values in the first and second security fields by respective third and fourth amounts resulting in respective third and fourth adjusted values;

wherein the security fields have write restrictions associated with them which prevent values in the security fields being adjusted:

in accordance with the first message, if the initial values have been adjusted in accordance with the second message; and

in accordance with the second message, if the initial values have been adjusted in accordance with the first message; and

wherein the action is only performed when the initial values in the security fields have been adjusted in accordance with the first message, and

the first and second adjusted values are different than the third and fourth adjusted values, respectively.

Claims 11-14. (Cancelled)

Claim 15. (Currently Amended) A method according to claim 2, wherein ~~the target entity is a first integrated circuit and the~~ messages are sent by a second integrated circuit.

Here ends the Examiner's amendment to the claims.

Allowable Subject Matter

9. Claims 2-7 and 15-18 are allowed over the prior art of record. The added limitations concerning the adjusting of values and creating write restrictions in the first and second security fields overcomes the prior art. Specifically, the limitation, "the first and second adjusted values are different than the third and fourth adjusted values, respectively." The combination of the Serizawa patent and the Kane patent deals with the sending of a first update message and the resending of the same message if the first sending is not received. The amended limitations make it clear that the first and second messages of the instant invention are not the different messages. A further search of the prior art has not uncovered any applicable reference.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to WILLIAM S. POWERS whose telephone number is (571)272-8573. The examiner can normally be reached on m-f 8:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kambiz Zand can be reached on 571 272 3811. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/W. S. P./
Examiner, Art Unit 2434

William S. Powers
Examiner
Art Unit 2434

9/20/2009

/Kambiz Zand/

Supervisory Patent Examiner, Art Unit 2434

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